



PATENT APPLICATION  
Do. No. 8371-127

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Apostolos VOUTSAS and Yukihiko NAKATA

Serial No. 09/945,063

Examiner: George R. Fourson III

Filed: August 31, 2001

Group Art Unit: 2823

For: **METHOD OF FORMING MULTI-LAYERS FOR A THIN FILM  
TRANSISTOR (TFT) AND THE DEVICE FORMED THEREBY**

Box Non-Fee Amendment  
Assistant Commissioner for Patents  
Washington, D.C. 20231

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TRANSMITTAL LETTER

Responsive to the Office Action dated October 31, 2002, enclosed is an amendment in the above-identified application.

- [X] Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

Hosoon Lee  
Limited Recognition Under 37 CFR § 10.9(b)

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I HEREBY CERTIFY THAT THIS COPY OF THE PATENT APPLICATION IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO:  
1. COMMISSIONER OF PATENTS AND TRADEMARKS, WASHINGTON, D.C. 20231  
X 2. ASSISTANT COMMISSIONER FOR PATENTS, WASHINGTON, D.C. 20231  
3. ASSISTANT COMMISSIONER FOR TRADEMARKS, 2500 CRISTAL DRIVE, FARMINGTON, VA 22031

Dec. 2, 2002

Judy Hignmore